

F I G. 1

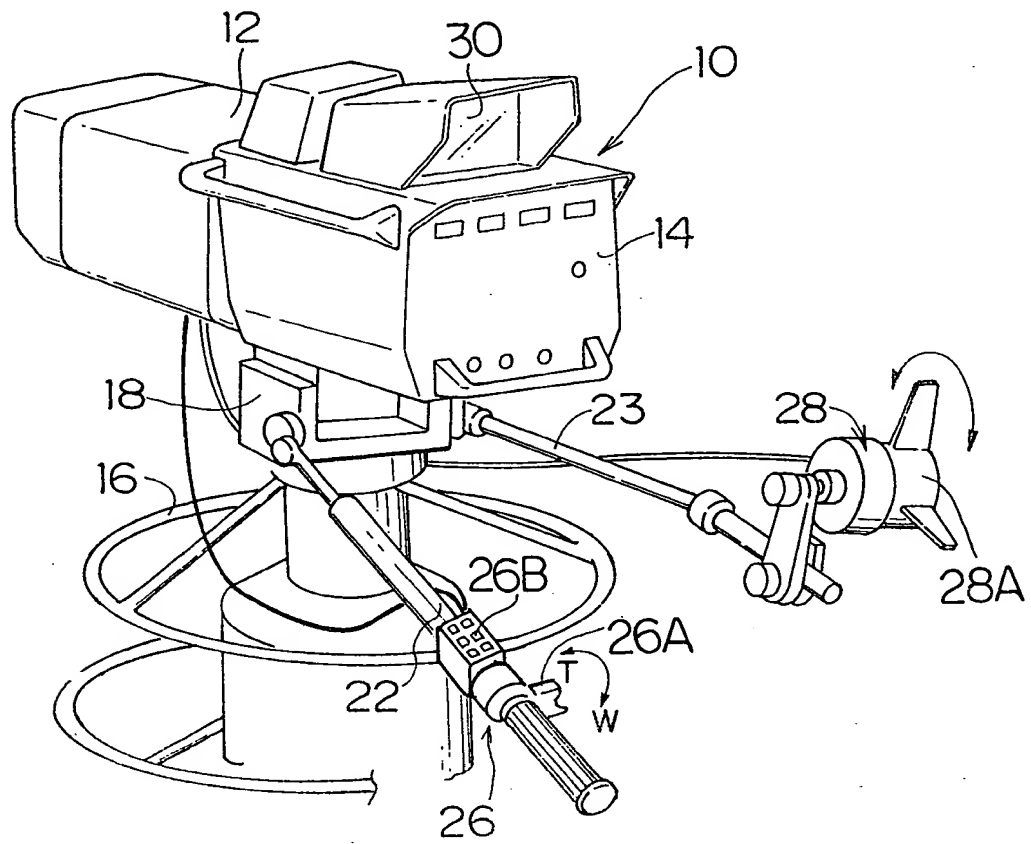
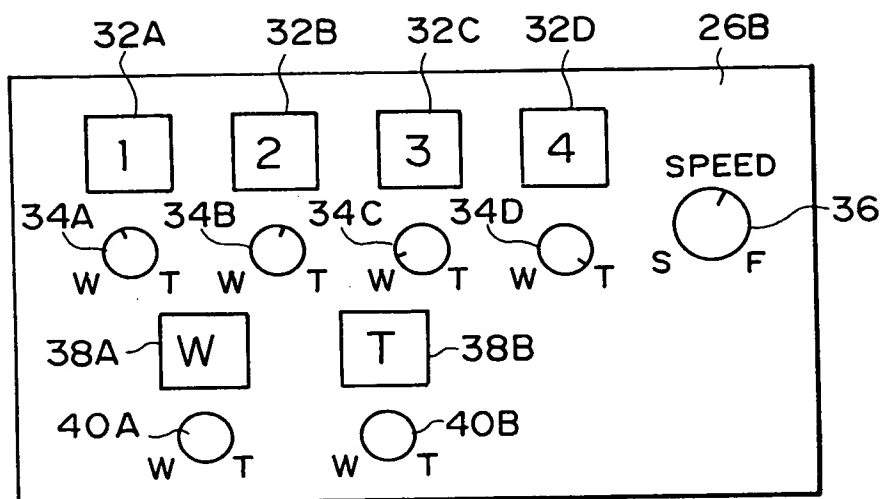


FIG. 2



F1G. 3

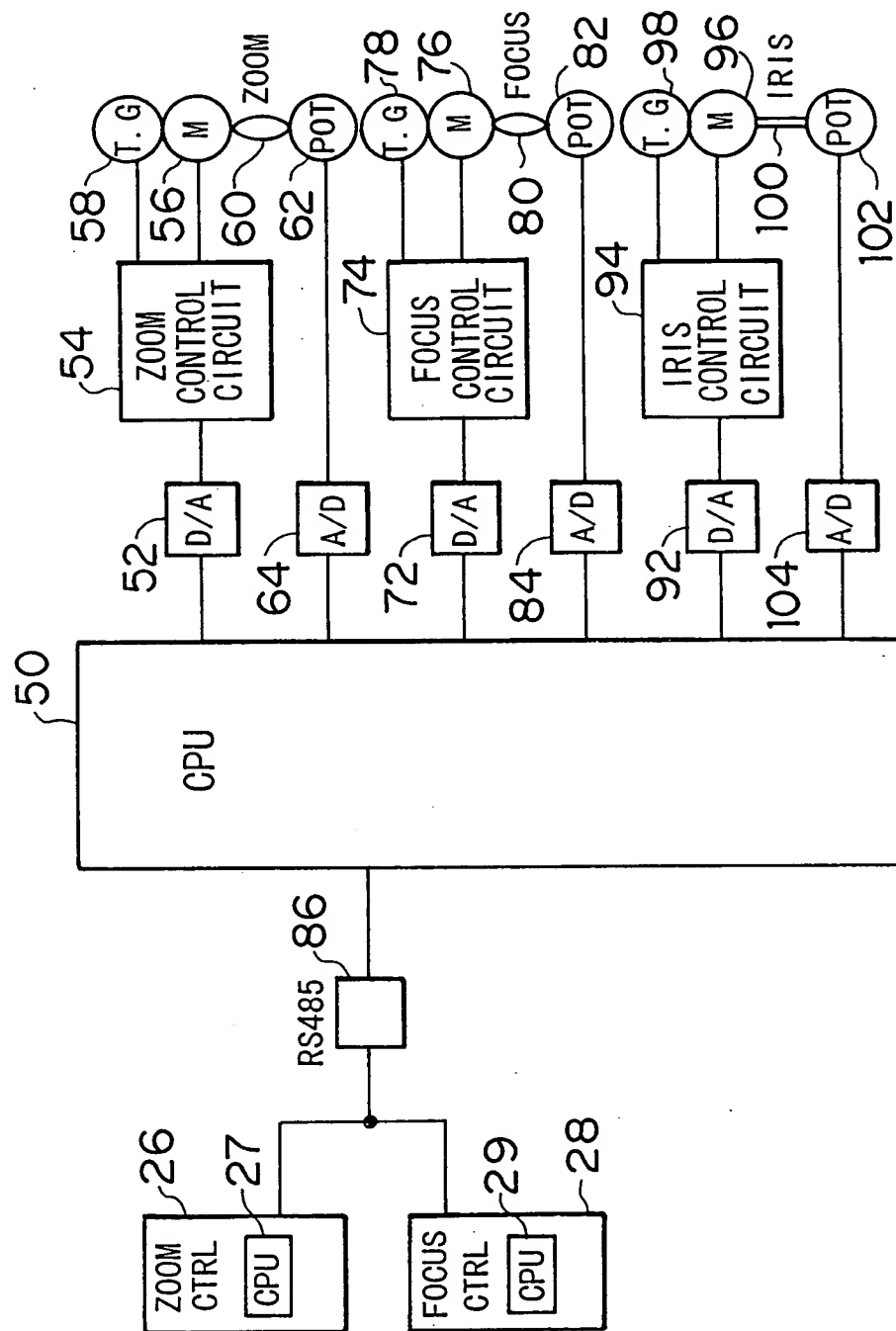
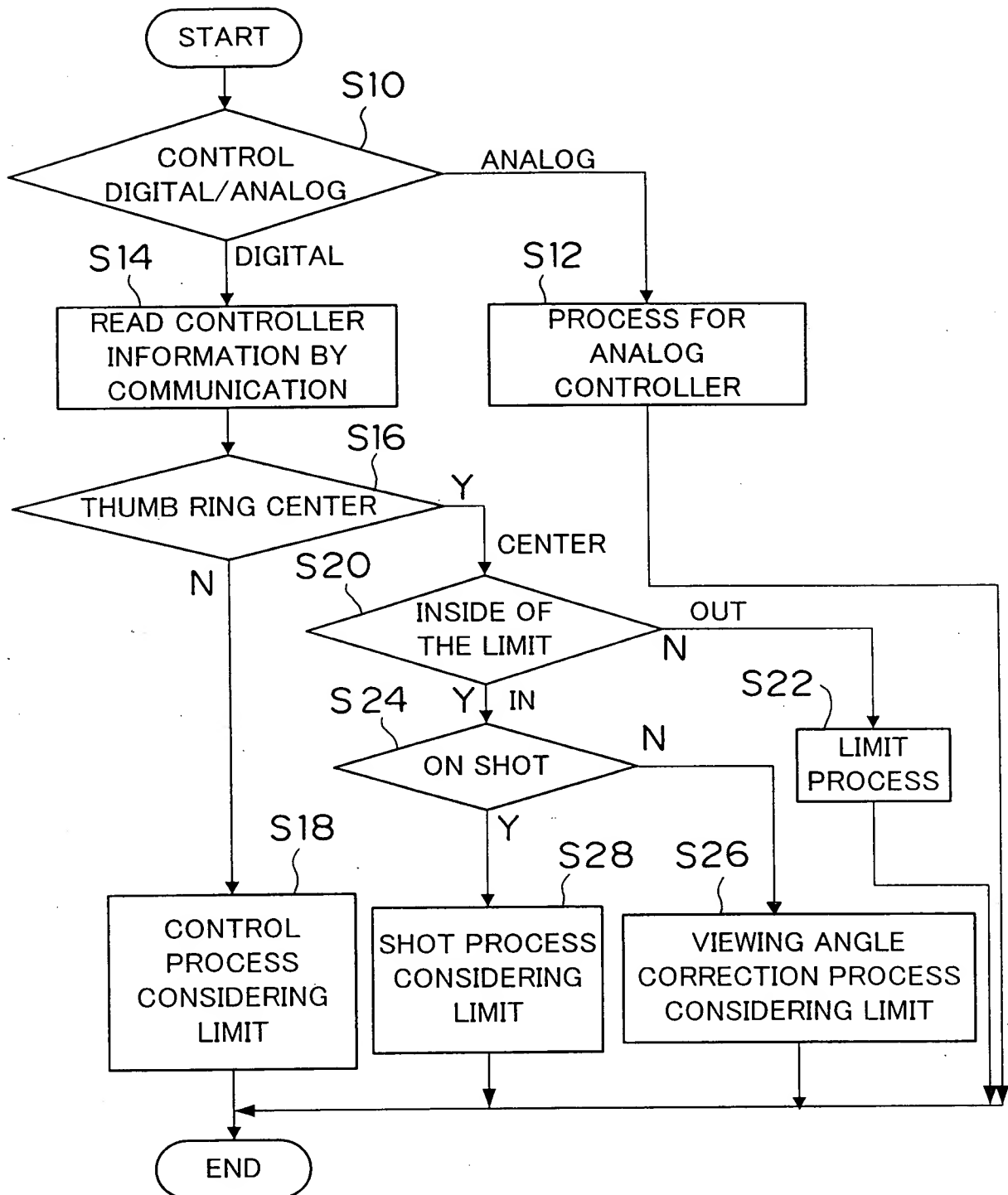


FIG. 4

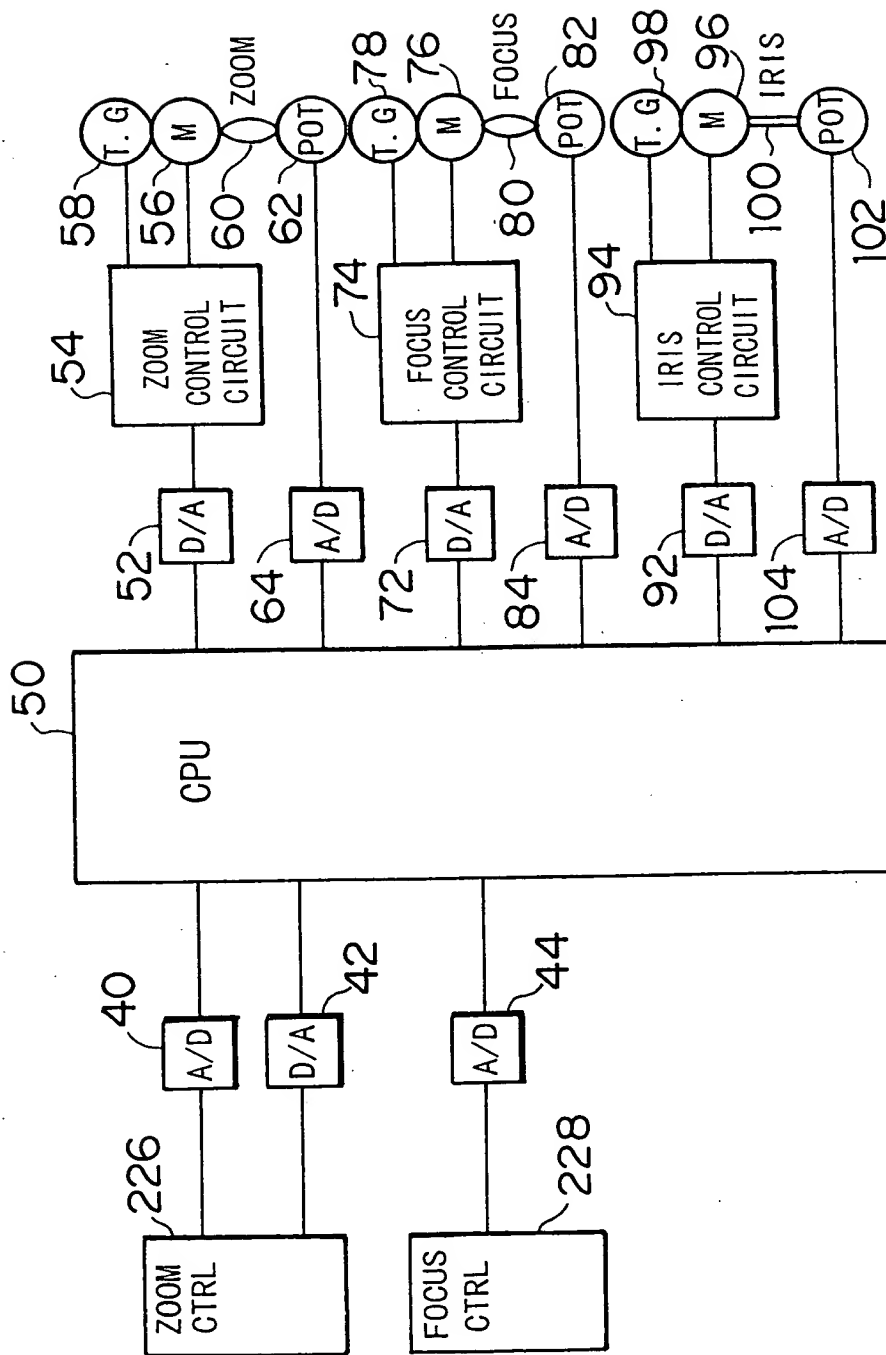


The diagram shows a central CPU (50) connected to three main control systems: Zoom, Focus, and Iris.

1. **Zoom Path:** The CPU (50) sends signals to a Zoom Ctrl block (226) and a D/A converter (40). The Zoom Ctrl block (226) outputs to a D/A converter (42). The D/A converter (42) outputs to a Zoom Control Circuit (54). The Zoom Control Circuit (54) outputs to a D/A converter (52). The D/A converter (52) outputs to a T.G. (58) and an M (56) component. The M (56) component outputs to a Zoom (60) component. The Zoom (60) component outputs to a P.O.T. (62).

2. **Focus Path:** The CPU (50) sends signals to a Focus Ctrl block (228) and a D/A converter (44). The Focus Ctrl block (228) outputs to a D/A converter (44). The D/A converter (44) outputs to a Focus Control Circuit (74). The Focus Control Circuit (74) outputs to a D/A converter (72). The D/A converter (72) outputs to a T.G. (78) and an M (76) component. The M (76) component outputs to a Focus (80) component. The Focus (80) component outputs to a P.O.T. (82).

3. **Iris Path:** The CPU (50) sends signals to an Iris Control Circuit (94). The Iris Control Circuit (94) outputs to a D/A converter (92). The D/A converter (92) outputs to a T.G. (98) and an M (96) component. The M (96) component outputs to an Iris (100) component. The Iris (100) component outputs to a P.O.T. (102).



F. I. G. 6

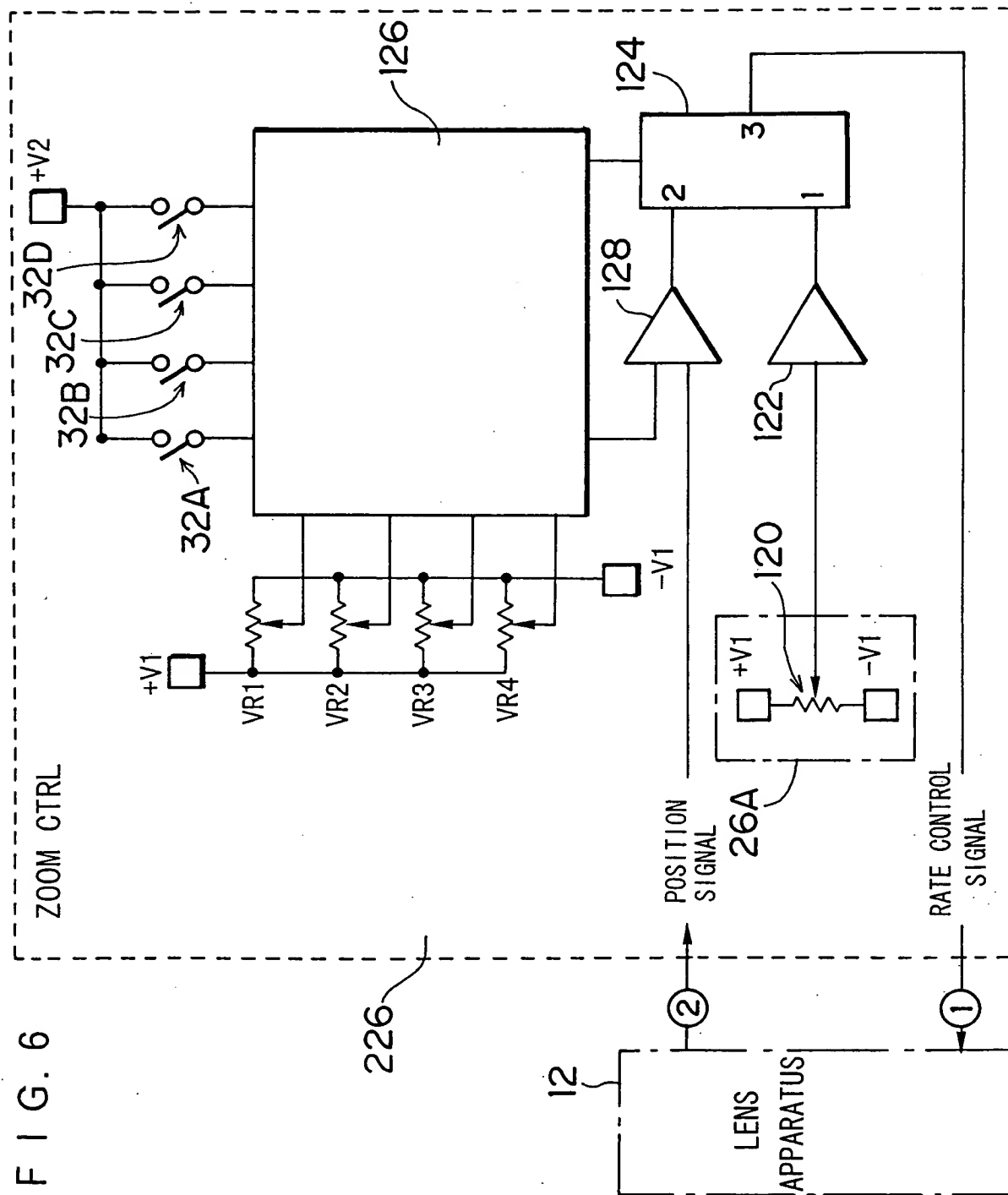


FIG. 7

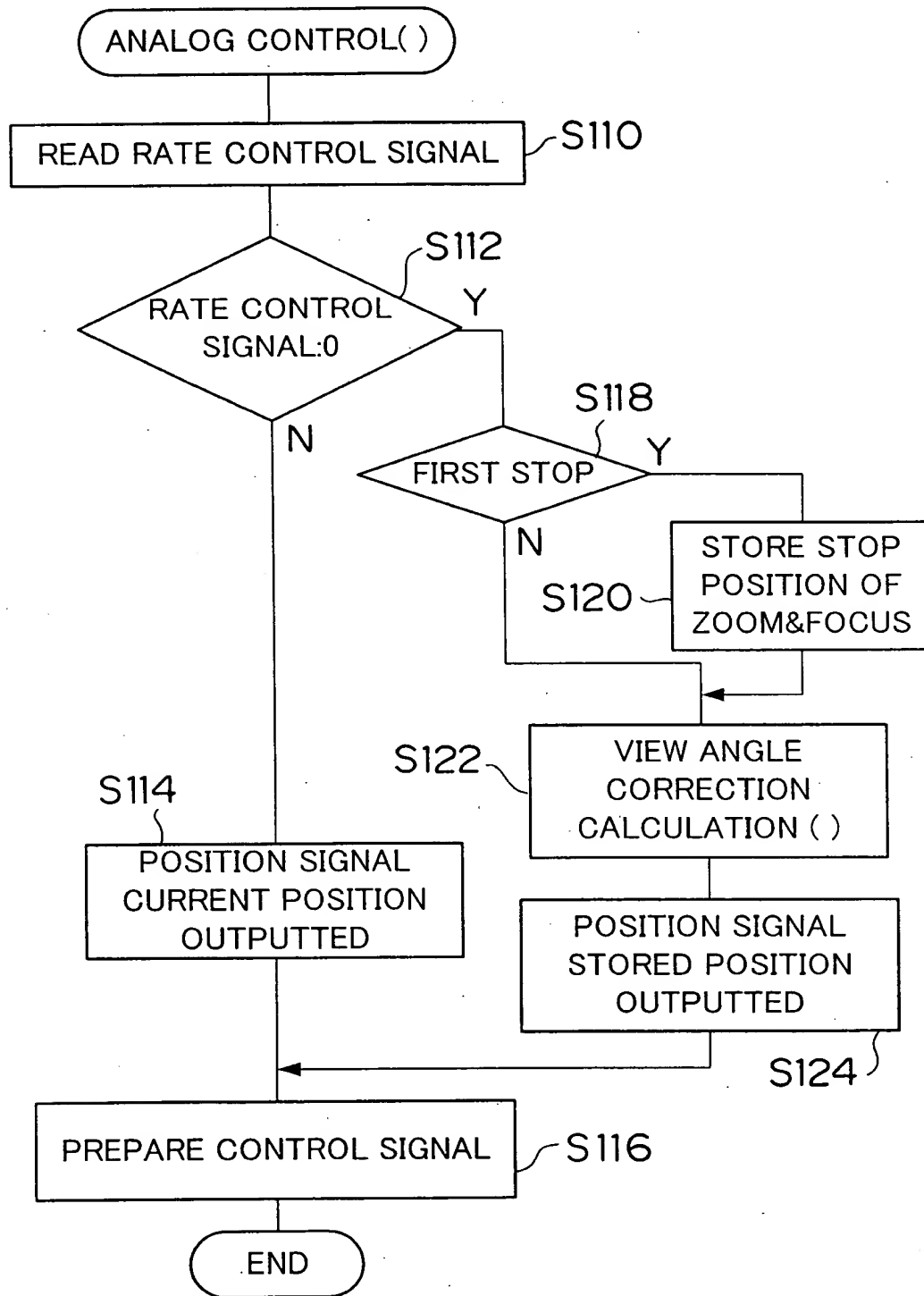


FIG. 9

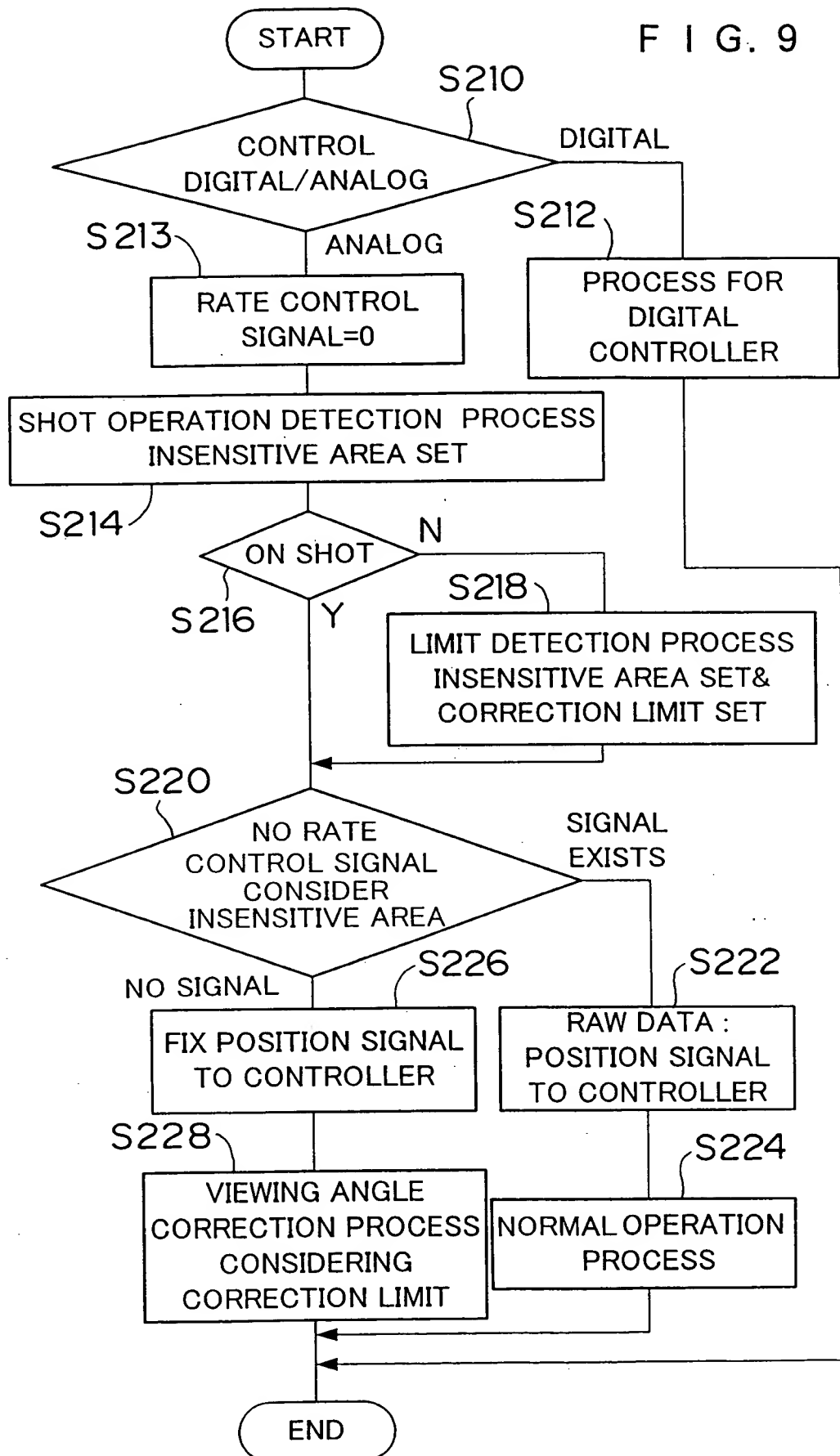


FIG. 10

